

ClioSoft to Exhibit Enterprise IP & Design Collaboration Platform at CDN Live 2014 -- Boston and Austin

FREMONT, Calif., September 15, 2014 – ClioSoft Inc., a leader in hardware configuration management solutions for the semiconductor design industry, will demonstrate its SOS Design Collaboration Platform seamlessly integrated with Cadence's Virtuoso® Custom IC flow at CDN Live 2014 (Boston and Austin).

WHAT: ClioSoft provides hardware configuration management for analog, digital and mixed-signal designs, streamlining the design process by enhancing communication and facilitating efficient and accurate sharing of design data from concept through tape-out. The tight integration of ClioSoft's [SOS viaDFII](#) with the Cadence Virtuoso design flow improves design team productivity, reduces the chance of mask re-spins due to configuration errors and makes design reuse more efficient. [Visual Design Diff](#) gives users the power to quickly compare two versions of a schematic or layout by graphically highlighting the differences directly in their own design editor. Users can also invoke a hierarchical diff where all differences for an ECO or between releases, for the entire design hierarchy, will be flagged. ClioSoft also will showcase its enterprise IP management solution, which empowers design companies to store, govern and propagate the IPs.

WHERE AND WHEN:

[CDNLive Boston](#) September 16, 2014

Westford Regency Inn and Conference Center,
219 Littleton Rd Westford, MA 01886

[CDNLive Austin](#) September 18, 2014

Hilton Austin
500 E 4th St, Austin, TX 78701

About ClioSoft

[ClioSoft](#) is the premier developer of hardware configuration management (HCM) solutions for analog/ mixed-signal and digital designs. The company's [SOS Design Collaboration Platform](#) is built to handle the complex requirements of system-on-chip (digital, analog, and mixed-signal) design flows. The SOS platform provides a sophisticated multi-site development environment that enables global team collaboration and efficient management of design data from concept through tape-out along with an enterprise IP management and design reuse solution. SOS is

integrated with leading design flows –Cadence’s Virtuoso® technology, Keysight Technologies’ Advanced Design System (ADS), Mentor Graphic’s Pyxis Custom IC Design, Synopsys’ Galaxy Custom Designer® and Laker3™ Custom Design.

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